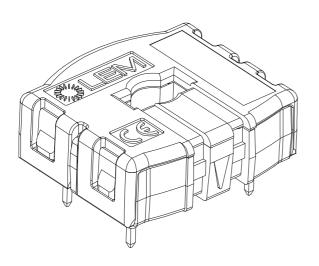


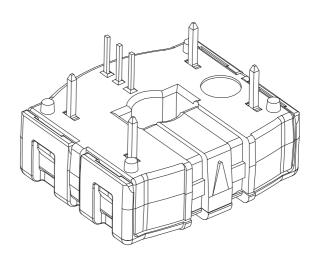
AUTOMOTIVE CURRENT TRANSDUCER HC2F250-S CLIPS

CE











Introduction

The HC2F CLIPS Family is for use on the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HC2F CLIPS family gives you the choice of having different current measuring ranges in the same housing.

Features

- Open Loop transducer using the Hall effect
- · Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range from 80 A up to 250 A
- Maximum rms primary admissible current: defined by busbar, the magnetic core or the ASIC to have T° < + 150°C
- Operating temperature range: 40°C < T° < + 125°C
- Output voltage: full ratio-metric (in gain and offset)
- · Compact design for PCB mounting.

Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- Very low thermal gain drift
- · Wide frequency bandwidth
- No insertion losses.

Automotive applications

- · Electrical Power Steering
- Starter Generators
- Converters ...

Principle of HC2F CLIPS Family

The open loop transducers use an Hall effect integrated circuit. The magnetic flux density B, contributing to the rise of the Hall voltage, is generated by the primary current I_p to be measured. The current to be measured I_p is supplied by a current source i.e. battery or generator (Fig. 1).

Within the linear region of the hysteresis cycle, B is proportional to:

$$B(I_{p}) = constant(a) \times I_{p}$$

The Hall voltage is thus expressed by:

$$V_{H} = (R_{H}/d) \times I \times constant (a) \times I_{P}$$

Except for $\mathbf{I}_{\mathrm{p}},$ all terms of this equation are constant. Therefore :

$$V_{H}$$
 = constant (b) x I_{P}

The measurement signal $V_{\rm H}$ amplified to supply the user output voltage or current.

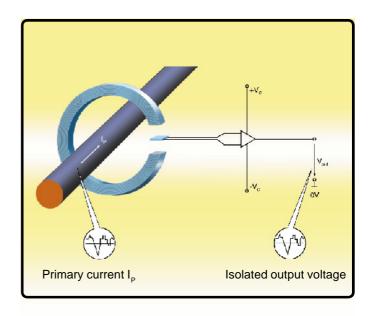


Fig. 1: Principle of the open loop transducer

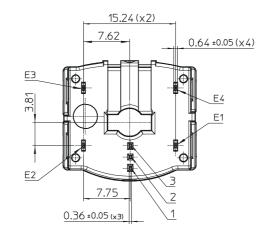


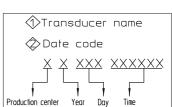
Dimensions HC2F xxx-S CLIPS family (in mm. 1mm = 0.0394 inch)

Secondary connection

Terminals	Designations
3	Supply voltage + 5 V DC
1	V _{out}
2	Ground
E1 to E4	Ground (*)

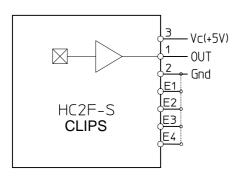
(*) Only 1 of these 4 pins could be connected

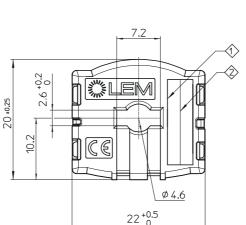




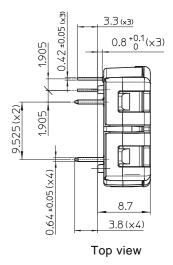
Positive primary current direction

Connection





Rear view



Bill of materials

Plastic case
Magnetic core
Pins
Copper alloy tin platted (lead free)

Mass 6 g

Remarks

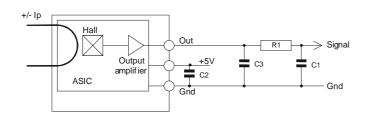
Right view

General tolerance

± 0.2 mm

• $V_{OUT} > \frac{V_c}{2}$ when I_p flows in the direction of the arrow.

Electronic schematic



Power supply decoupling capacitor : C2 = 47 nF EMC protection capacitor : C3 = 4.7 nF

Optional:

High frequency signal noise filter:

R1 > 100 ohms

C1 = defined according to the system frequency bandwidth



Absolute maximum ratings (not operating)

Parameter	Symbol	Unit	Specification	Conditions
Maximun peak primary current (not operating)	I _{P max}	А	Defined by busbar to have T° ≤ 150°C	
Primary nominal DC or current rms	I _{PN}	А	Defined by busbar to have T° ≤ 150°C	
Maximun supply voltage (not operating)	V _{C max}	V	7	
Secondary maximum admissible power	P _{S max}	mW	150	
Ambient operating temperature	T _A	°C	- 40 < T _A < 125°C	
Ambient storage temperature	T _S	°C	- 40 < T _S < 125°C	
Electrostatic discharge voltage	V _{ESD}	V	2000	see page 5/5
Maximum admissible vibration	γ	m.s ⁻²	100	see page 5/5
Rms voltage for AC isolation test 50 Hz, 1 min	V _d	V	2000	

Operating characteristics

	Symbol Unit		Specification			Conditions		
			Min	Typical	Max	Conditions		
Electrical Data								
Primary current, measuring range	\mathbf{I}_{PM}	Α	-250		250	@ - 40°C < T° < 125°C		
Supply voltage	\mathbf{V}_{C}	V	4.75	5.00	5.25	@ - 40°C < T° < 125°C		
Output voltage (Analog) 1)	$\mathbf{V}_{\mathrm{out}}$	V	$V_{OUT} = (V_C/5) \times (2.5 + 0.008 \times I_P)$		·0.008 x I _P)	@ - 40°C < T° < 125°C		
Sensitivity 1)	G	V/A	0.0078	0.008	0.0082	@ T _A = 25°C		
Offset voltage 1)	V _O	V	2.477	2.5	2.523	@ $V_C = 5.00 \text{ V}$; $T_A = 25^{\circ}\text{C}$; $I_P = 0 \text{ A}$		
Current consumption	I _c	mA	-	13.5	17	@ - 40°C < T° < 125°C; 4.75 V < V _C < 5.25 V		
Load resistance	R_L	ΚΩ	2	-	-			
Output internal resistance	\mathbf{R}_{OUT}	Ω	-	-	10			
Performance Data								
Sensitivity error	$\epsilon_{\scriptscriptstyle G}$	%	-2.0	± 0.7	2.0	@ $T_A = 25$ °C, $V_C = 5.00$ V; $Gth = 0.008$ V/A		
Electrical offset	I OE	Α	-1.5	± 0.63	1.5	@ $V_C = 5.00 \text{ V}; T_A = 25^{\circ}\text{C}$		
Electrical offset	\mathbf{V}_{OE}	mV	-12	± 5	12	@ $V_C = 5.00 \text{ V}; T_A = 25^{\circ}\text{C}$		
Magnetic offset	I _{OM}	Α	-1.4	± 0.9	1.4	@ After excursion to $\pm I_P$; $T_A = 25^{\circ}C$		
Magnetic offset	\mathbf{V}_{OM}	mV	-11.2	± 7.2	11.2	@ After excursion to $\pm I_P$; $T_A = 25^{\circ}C$		
I _{OE}	TCI _{OE AV}	mA/°C	-15.0	± 6.3	15.0	@ - 40°C < T° < 125°C; V _C = 5.00 V		
Average temperature coefficient of								
\mathbf{V}_OE	TCV _{OE AV}	mV/°C	-0.12	± 0.05	0.12	@ - 40°C < T° < 125°C; V _C = 5.00 V		
Average temperature coefficient of G	TCG AV	%/°C	-0.04	± 0.02	0.04	@ - 40°C < T° < 125°C; V _C = 5.00 V		
Linearity error	$\epsilon_{\scriptscriptstyle \! L}$	% I _P	-1.5	± 0.8	1.5	@ I _P ; V _C = 5.00 V, T _A = 25°C		
Response time	t _r	μs	-	15	20	@ di/dt = 50 A/μs; I _T = 100 A		
Frequency bandwidth 2)	BW	kHz	20	-	-	@ -3 dB; I _T = 40 A rms		
Output voltage noise peak-peak	V _{no p-p}	mV	-	13	18	@ T _A = 25°C; 0 Hz < f < 1 MHz		
Output voltage noise rms	V _{no rms}	mV	-	1.5	3	@ T _A = 25°C; 0 Hz < f < 1 MHz		

Notes: 1) The output voltage \mathbf{V}_{OUT} is fully ratio-metric and depends on the supply voltage \mathbf{V}_{C} . The \mathbf{V}_{C} value must be measured and used with the following formula:

$$Ip = \left(Vout - \frac{Vc}{2}\right) \times \frac{1}{G} \times \frac{5}{Vc} \quad \text{With G in (V/A)}$$

²⁾ Small signal only to avoid excessives heatings of the busbar, the magnetic core and the ASIC.



PERFORMANCE PARAMETERS DEFINITIONS

Linearity:

The output voltage noise is the result of the noise floor of the Hall elements and the linear I_c amplifier gain.

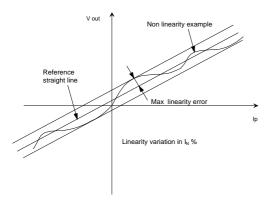
Magnetic offset:

The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of ${\bf I}_{\rm P\ max}$.

Linearity:

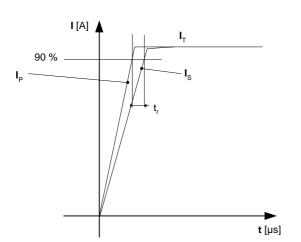
The maximum positive or negative discrepancy with a reference straight line $\mathbf{V}_{\text{OUT}} = \mathbf{f}(\mathbf{I}_{\text{p}})$.

Unit: linearity (%) expressed with full scale of Ip may



Response time (delay time) t,:

The time between the primary current signal and the output signal reach at $90\,\%$ of its final value.



Typical:

Theorical value or usual accuracy recorded during the production.

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $V_{OLIT} = f(I_p)$, it must establish the relation:

 $V_{OUT}(I_p) = (V_c/5) \times (G \times I_p + 2.5)$ (*).

* For all symetric transducers.

Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25°C.

The sensitivity variation \mathbf{G}_{T} is the maximum variation (in ppm or %) of the sensitivity in the temperature range:

 \mathbf{G}_{T} = (Sensitivity max - Sensitivity min) / Sensitivity at 25°C. The sensitivity drift $\mathbf{TCG}_{\mathrm{AV}}$ is the \mathbf{G}_{T} value divided by the temperature range.

Offset voltage @ Ip = 0 A:

Is the output voltage when the primary current is null. The ideal value of $\mathbf{V}_{\rm O}$ is $\mathbf{V}_{\rm C}/2$ at $\mathbf{V}_{\rm C}=5$ V. So, the difference of $\mathbf{V}_{\rm O}$ - $\mathbf{V}_{\rm C}/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

Offset with temperature:

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25°C.

The offset variation \mathbf{I}_{OT} is a maximum variation the offset in the temperature range:

 $\mathbf{I}_{\mathsf{OT}} = \mathbf{I}_{\mathsf{OE}} \; \mathsf{max} - \mathbf{I}_{\mathsf{OE}} \; \mathsf{min}$

The Offset drift ${\bf TCI}_{\rm OEAV}$ is the ${\bf I}_{\rm OT}$ value divided by the temperature range.

Environmental test specifications

Name	Standard	Conditions				
Low T° storage	IEC 60068 Part 2-1	T° - 40°C / 100 H not connected				
Thermal shocks	IEC 60068 Part 2-14	T° - 30°C to 110°C /1000 cycles not connected				
Low T°operation at min supply voltage	IEC 60068 Part 2-1	T° - 40°C / 1000 H supply voltage = 4.75 V				
Hight T° operation at max supply voltage	IEC 60068 Part 2-2	T° 125°C / 1000 H supply voltage = 5.25 V				
Temperature humidity bias	IEC 60068 Part 2-3	T° 90°C / 95 % RH/ 1000 H supply voltage = 5.25 V				
Pressure cooker		T° 125°C / 100 % RH, P 0.178 Mpa/100 H supply voltage = 5 V				
Mechanical Tests						
Vibration	IEC 60068 Part 2-64	Room T°, acceleration 100 m/s2, frequency 20 to 500 Hz/96 H each axis				
Drop test	IEC 60068 Part 2-29	Height 750 mm concrete floor each directions				
EMC Test						
Electrostatic discharge	JESD22-A114-B	Applied voltage = ± 2 kV pin to pin number of discharge = 1				